

Optimized Compensation of Unwanted Current Terms by AC Power Converters Under Generic Voltage Conditions

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Abstract—This paper formulates an optimization-based algorithm for the compensation of unwanted current terms by means of distributed electronic power converters, such as active power filters and grid-connected inverters. The compensation goal consists in achieving suitable load conformity factors, defined at the source side and within a feasible power region in terms of the power converter capability. Based on the measured load quantities and on a certain objective function, the algorithm tracks the expected source currents, which are thereupon used to calculate proper scaling coefficients and, therefore, the compensation current references. It improves the power quality at the point of common coupling and enables full exploitation of distributed energy resources, increasing their efficiency. The compensation is based on a decoupled current decomposition and on an optimization-based algorithm. In this paper, the strategy is applied to nonlinear and unbalanced three-phase four-wire circuit, under nonsinusoidal and asymmetrical voltage conditions. The steady-state and dynamic behaviors have been analyzed by theoretical, simulation, and experimental results. Furthermore, the proposed approach is also compared to other compensation strategies showing its effectiveness.

Index Terms—Distributed power generation, nonsinusoidal voltage, optimized compensation, power filters, power quality.

I. INTRODUCTION

NOWADAYS, the use of electronic power processors (EPPs) for interfacing distributed energy resources (DERs) may be understood as a waste of power electronic capability, if only to inject active power from the primary energy source (PES) into the grid [1], [2]. Particularly in case of

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renewable PESs, the intermittent power generation may lead the EPPs to have idle power capacity during significant periods of operation. Thus, multifunctional inverters have been proposed to perform ancillary tasks, such as the compensation of unwanted current terms (reactive, distortion, and unbalance) or voltage regulation, enabling full exploitation of EPPs capability, increasing their cost-benefit and improving the power quality, and efficiency of the power system [3], [4]. In this context, the Pacific Gas and Electric Company [5] has presented the first code in the area with the definition of “*smart inverter*” as an interfacing power converter that must be able to perform additional tasks, possibly following commands from the distribution provider.

Therefore, it is appealing to manage such EPPs following an optimized procedure that aims to attain, in terms of power quality, the best performance indices defined at the source side and within a previous defined feasible power region.

Most EPP’s control schemes can be classified, e.g., based on their reference frame, in synchronous ($dq0$), stationary ($\alpha\beta0$), and natural (abc), and such choice is related to the adopted control strategy, the implemented controllers, and the voltage conditions [6], [7]. In [8], an optimal control scheme based on $\alpha\beta0$ -frame and voltages processed through signal conditioning filters to extract the voltage sequence components was proposed. Kanjiya *et al.* [9], [10] proposed a selective frequency-domain optimal control based on the definition of conductance factors in abc -framework.

Recently, Alfonso-Gil *et al.* [11] addressed an optimized compensation strategy based on linear matrix inequalities and using the IEEE Standard 1459 to selectively identify the unwanted power terms. It showed superior performance to [12], which introduced a priority-based compensation strategy. However, as the Standard 1459 was not proposed for compensation purposes, that technique is not able to fully decouple load and voltage non-idealities under deteriorated voltage operation, which could lead to nonoptimized compensation (this scenario was not evaluated in [11]). Kanjiya *et al.* [9] proposed a noniterative optimal control strategy based on the IEEE Standard 519, proving to be effective for dynamic load compensation, requiring one to two cycles (≈ 30 ms) to achieve the steady state. However, it does not measure the filter currents, which hampers the filter protection during short-circuit. Moreover, it performs *sinusoidal current synthesis* that may not provide damping capacity against induced resonances [13]. Both recent papers had not dealt with optimized compensation using DERs, on which compensation

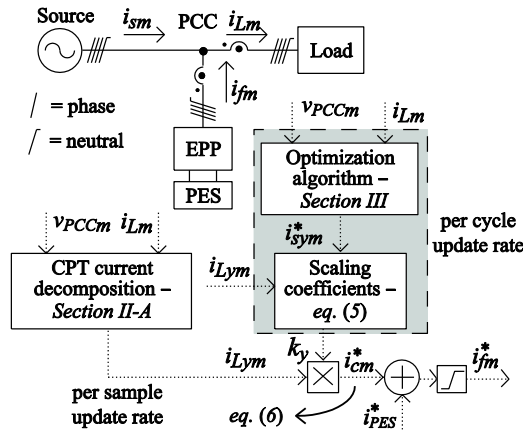


Fig. 1. Three-phase four-wire circuit with the block diagram of the optimized compensation strategy.

capability may vary over time [14], [15]. So, this paper contributes to fill such gap.

Furthermore, many papers based on noniterative optimization-based algorithms (OBAs) have argued that iterative OBAs are difficult to implement and/or present inherent computational delay [9]. However, iterative algorithms have a wide range of linear and nonlinear methods, e.g., Simplex method (SM), interior-point method, quadratic method, primal method, among others, applied to digital implementation [16]. For example, the *revised Simplex* is a variant of SM, in which the main attributes are the economy of memory and computational processing [16].

Therefore, the main goal of this paper is to optimize the compensation of reactive, harmonic, and unbalance current terms, by means of the idle power capacity of distributed EPPs, especially due to intermittent behavior of PES. The proposed compensation uses SM, though any other programming solver could be applied to, and the *conservative power theory* (CPT) [17]. This latter has been chosen due to its orthogonal current decomposition, which decouples the unwanted load current terms, allowing to selectively compensating them, even under nonsinusoidal voltages. Besides, the *CPT's load conformity factors* [18] were used to define the power quality constraints, at the source side, such as reactive and unbalance current limits and minimum distortion and power factor requirements, which are rarely investigated [19]–[21].

The optimized compensation is devised as a linear iterative algorithm using standard SM, which has been chosen because of its simplicity of implementation, and good and fast results for problems with limited number of variables and constraints. The proposed method has only one objective function and, thanks to the decoupled CPT's current decomposition, there are no trade-offs between conflicting objectives, as usual in multiobjective optimization programming [22], [23].

Fig. 1 shows the simplified schematic of a three-phase four-wire circuit, considering the block diagram of the optimized compensation strategy to generate the optimal compensation current references (i_{cm}^*). The load quantities are measured and used to process the CPT's current terms (i_{Lym}) and the optimization algorithm. The converter output currents (i_{fm}) are

also measured and used into the current control loop, as usually employed in EPPs [6], though not shown in Fig. 1.

The optimization block returns the expected source currents (i_{sym}^*), which are thereupon used to calculate the scaling coefficients (k_y) and, therefore, the compensation current references to be applied to EPP's current control loop.

This paper is an extended version of [24], emphasizing a description of the state of the art, analyzing operation under distorted and asymmetrical voltages, presenting further simulation and experimental results and comparing the proposed approach with other proposals in the literature.

II. REQUIRED BACKGROUND OF CPT

CPT [17] is a time-domain power-based theory, valid for single- and three-phase systems, with three- or four-wire circuits, independent of the purity of voltage and current waveforms, as required by other power theories [25].

A. Current Decomposition

CPT splits the instantaneous currents into different orthogonal current terms, which are directly related to *load behavior*. Let us assume a multiphase circuit where each particular phase of the system is denoted by the subscript “ m ”:

- 1) *balanced active current* (i_{am}^b) is the term related to the active power consumption (useful work);
- 2) *balanced reactive current* (i_{rm}^b) is the term related to the reactive power circulation (i.e., *reactive energy*);
- 3) *void current* (i_{vm}) is the term related to the nonlinear (distortion) behavior between voltages and load currents;
- 4) *unbalanced current* (i_{um}^u) is the term related to the unbalanced load among the system's phases;
- 5) *nonactive current* (i_{nam}) is the term that represents all the unwanted terms of the current.

By definition, the collective rms current (in bold) can be split into

$$\mathbf{I}^2 = \mathbf{I}_a^{b2} + \mathbf{I}_{na}^2 = \mathbf{I}_a^{b2} + \mathbf{I}_r^{b2} + \mathbf{I}_v^2 + \mathbf{I}^u{}^2. \quad (1)$$

Note that each current term is orthogonal to each other. Thus, multiplying the collective rms current and voltage, the apparent power A can be decomposed into

$$A^2 = V^2 \cdot \mathbf{I}^2 = P^2 + Q^2 + D^2 + N^2 \quad (2)$$

where P is the active power, Q is the reactive power, D is the distortion power, and N is the unbalance power. Complementary analyses of the current and power components can be found in [17].

B. Load Conformity Factors

Considering the previous definition, the main idea behind the CPT is to identify power and current terms directly proportional to load characteristics, such as unbalance, nonlinearities, and reactivity, independent of supply voltage conditions. So, the CPT's conformity factors follow the same concept, focusing on the identification and quantification of the load phenomena. Thus, in order to characterize different aspects of load behavior, the load conformity factors were proposed in [18] as follows:

- 1) *power factor* (λ) is a general polyphase efficiency ratio, which is affected by reactive power, unbalanced loads, and nonlinearities. Unity power factor represents current waveforms proportional to voltage waveforms (as in case of balanced resistive loads);
- 2) *reactivity factor* (λ_Q) reveals the presence of reactive energy in linear inductors or capacitors, or even phase shifts due to nonlinear loads (e.g., thyristor rectifiers);
- 3) *distortion factor* (λ_D) indicates the presence of voltage and current's nonlinearities;
- 4) *unbalance factor* (λ_N) indicates the effect of load unbalance.

Except for the power factor, all the other conformity factors are zero for ideal conditions, regardless of the voltage distortion or symmetry

$$\lambda = \frac{I_a^b}{\sqrt{I_a^{b^2} + I_{na}^2}} = \frac{I_a^b}{I} = \frac{P}{A} \quad (3.a)$$

$$\lambda_Q = \frac{I_r^b}{\sqrt{I_a^{b^2} + I_r^{b^2}}} = \frac{Q}{\sqrt{P^2 + Q^2}} \quad (3.b)$$

$$\lambda_D = \frac{I_v}{\sqrt{I_a^{b^2} + I_r^{b^2} + I_v^2 + I^u^2}} = \frac{I_v}{I} = \frac{D}{A} \quad (3.c)$$

$$\lambda_N = \frac{I^u}{\sqrt{I_a^{b^2} + I_r^{b^2} + I^u^2}} = \frac{N}{\sqrt{P^2 + Q^2 + N^2}}. \quad (3.d)$$

From (3), the load conformity factors are related to the power factor as in

$$\lambda = \sqrt{(1 - \lambda_Q^2) \cdot (1 - \lambda_D^2) \cdot (1 - \lambda_N^2)}. \quad (4)$$

C. Relation of CPT's Load Conformity Factors to Conventional Power Quality Indices

Under ideal voltage conditions, for single- and three-phase circuits, the CPT's factors lead to the same conclusions and values of the conventional power quality indices, such as total harmonic distortion (THD), unbalance factors, and displacement factor. Thus, considering sinusoidal and symmetrical voltages, λ results equal to the traditional fundamental displacement factor ($\cos \phi_1$), where ϕ_1 is the phase angle between fundamental phase voltage and current. For single- or balanced three-phase circuits, λ_Q could be calculated as $\lambda_Q = \sin(\phi_1)$. λ_D can be associated with the conventional current THD_i by $\lambda_D = \text{THD}_i / \sqrt{1 + \text{THD}_i^2}$, and finally, λ_N can be related to the traditional positive, negative, and zero sequence unbalance factors [26].

However, if the voltages are not sinusoidal, the CPT's factors show how a generic load circuit may affect the current and power terms at the PCC. In such a case, λ_D may be slightly affected by the scattering phenomena (such as skin effect) imposed by distorted voltages [17], but such an influence is rather limited as the voltage distortions (THD) were lower than 10%. Besides, the information from such conformity factors is related to the entire polyphase circuit, and not only to single-phase variables.

Thus, the trick in handling the CPT to define the compensation current references and the load conformity factors to set the constraints, instead of conventional power quality, is that the CPT's factors are concentrated on the load characteristics and not just on the current waveforms, which can be highly influenced by voltage conditions.

D. Flexible and Selective Generator of Compensation Currents

The generator of compensation currents adjusts some coefficients to scale the magnitude of the decomposed CPT's current terms, within any percentage. Thanks to their orthogonal characteristic, it is possible to minimize individually and accurately any of the unwanted current terms.

Based on Kirchhoff's current law, each scaling coefficient is a ratio of its collective load current term. By definition, they must range from 0 to 1

$$k_y = \frac{I_{Ly} - I_{sy}^*}{I_{Ly}}, \quad 0 \leq k_y \leq 1 \quad (5)$$

such that the superscript "*" means desired (reference) value, and the subscript "y" can assume Q , D , or N to represent the balanced reactive, distortion, or unbalance scaling coefficients.

To generate the EPP compensation current reference i_{cm}^* for each phase, according to the notation criteria defined in Fig. 1, it has been defined as

$$i_{cm}^* = k_Q \cdot i_{Lrm}^b + k_D \cdot i_{Lvm} + k_N \cdot i_{Lm}^u = k_{na} \cdot i_{Lnam}. \quad (6)$$

Note that unity scaling coefficients correspond to full compensation because the references turn equal to the nonactive load currents i_{Lnam} , allowing EPP to cancel them. The zero scaling coefficients mean no compensation ($i_{cm}^* = 0$).

From (5) and (6), one can note that to generate the compensation current references, it is enough to find the source currents (I_{sy}^*), which are the aim of the optimization algorithm of Fig. 1, and it is shown in Section III.

III. OPTIMIZED COMPENSATION BASED ON LINEAR PROGRAMMING PROBLEM

The standard model of SM is

$$\begin{aligned} (\min) \quad & z = \underline{c} \cdot \underline{x} \\ \text{s.t.} \quad & \underline{A} \cdot \underline{x} \leq \underline{b} \\ & \underline{x} \geq \underline{0} \end{aligned} \quad (7)$$

where z is the objective function and the underlined variables are matrices; \underline{c} is the matrix of the objective function coefficients; \underline{A} is the matrix of the constraint coefficients; and \underline{b} is the matrix of the limits. The last constraint requires that all the variables (\underline{x}) are nonnegative.

A. Linearization

To preserve the orthogonality among the decomposed CPT's current terms, the variables of the linear problem are defined as

the collective squared values of each source current term, which are all nonnegative variables, as follows:

- 1) X_P : collective squared value of the source balanced active currents ($\mathbf{I}_{sa}^{b^2}$);
- 2) X_Q : collective squared value of the source balanced reactive currents ($\mathbf{I}_{sr}^{b^2}$);
- 3) X_D : collective squared value of the source void currents (\mathbf{I}_{sv}^2);
- 4) X_N : collective squared value of the source unbalanced currents (\mathbf{I}_s^2).

Then, rewriting (3) based on the previous linear variables, the squared values of each load conformity factor are

$$\lambda^2 = \frac{X_P}{X_P + X_Q + X_D + X_N} \quad (8.a)$$

$$\lambda_Q^2 = \frac{X_Q}{X_P + X_Q} \quad (8.b)$$

$$\lambda_D^2 = \frac{X_D}{X_P + X_Q + X_D + X_N} \quad (8.c)$$

$$\lambda_N^2 = \frac{X_N}{X_P + X_Q + X_N} \quad (8.d)$$

B. Objective Function

The objective function is set to minimize the unwanted currents at the source side, while the weighting coefficients are used to assign relative importance to some terms over the others. These coefficients are represented in (9) by the squared values of each unwanted load current term $\underline{c} = [\mathbf{I}_{Lr}^b, \mathbf{I}_{Lv}, \mathbf{I}_L^u]$. Thus, the highest collective rms value, representing the most significant unwanted current term presented at the load side, is prioritized. It differs from other proposals as [11], [12]

$$(\min) z = X_Q \cdot \mathbf{I}_{Lr}^{b^2} + X_D \cdot \mathbf{I}_{Lv}^2 + X_N \cdot \mathbf{I}_L^u{}^2. \quad (9)$$

C. Constraints and Limits

Taking advantage of the formulated problem based on source-side quantities, it is possible to define a set of conformity factor constraints. It goes beyond of other approaches (e.g., [11]) that have defined only current limitation constraints, which are also taken into account here.

The conformity factor constraints are defined handling [8] and setting the factors as preset references, respectively,

$$X_Q + X_D + X_N \leq X_P \cdot \left(\frac{1 - \lambda^{*2}}{\lambda^{*2}} \right) \quad (10.a)$$

$$X_Q \leq X_P \cdot \left(\frac{\lambda_Q^{*2}}{1 - \lambda_Q^{*2}} \right) \quad (10.b)$$

$$-X_Q + X_D \cdot \frac{(1 - \lambda_D^{*2})}{\lambda_D^{*2}} - X_N \leq X_P \quad (10.c)$$

$$-X_Q + X_N \cdot \frac{(1 - \lambda_N^{*2})}{\lambda_N^{*2}} \leq X_P. \quad (10.d)$$

The reference values of the conformity factors may be set according to up-to-date standards relating the CPT's conformity factors as in Section II-C. Those references may be preset by

manufacturers based on the local standards or, online set by the distribution provider as per a *smart inverter*.

X_P can always be calculated according to Fig. 1

$$X_P = \mathbf{I}_{La}^{b^2} - \mathbf{I}_{PES}^{*2} \quad (11)$$

where \mathbf{I}_{PES}^* is the actual active current reference of PES. Naturally, for active power filter (APF) applications are about zero, whereas for DERs, \mathbf{I}_{PES}^* can be provided, for example, by maximum power point tracking techniques.

In addition, a last thermal constraint is needed due to the converter construction. The value of the collective rms current through EPP must not be higher than its nominal collective rate ($\mathbf{I}_f \leq \mathbf{I}_{nom}$). Thus, considering the polarities of Fig. 1, we have

$$(\mathbf{I}_L - \mathbf{I}_s) \leq \mathbf{I}_{nom}. \quad (12)$$

The active power injection takes priority over compensation. Then, the EPP available capability ($\Delta \mathbf{I}_f$) for current compensation is

$$\Delta \mathbf{I}_f = \sqrt{\mathbf{I}_{nom}^2 - \mathbf{I}_{PES}^{*2}}. \quad (13)$$

Of course, for APF applications, $\Delta \mathbf{I}_f$ is always approximately equal to its own nominal current.

Then, considering only the unwanted current terms and assuming top priority to the active current injection, (12) can be rewritten using (13) as

$$(\mathbf{I}_{Lna} - \mathbf{I}_{sna}) \leq \Delta \mathbf{I}_f \leftrightarrow \mathbf{I}_{sna} \geq (\mathbf{I}_{Lna} - \Delta \mathbf{I}_f). \quad (14)$$

Thus, after further rewriting (14), in terms of their squared values, the current limitation constraint is found

$$X_Q + X_D + X_N \geq (\mathbf{I}_{Lna} - \Delta \mathbf{I}_f)^2 \quad (15)$$

such that if $\Delta \mathbf{I}_f \geq \mathbf{I}_{Lna}$ means full compensation.

It is worth mentioning that (15) prevents the algorithm to converge, in steady state, to a point that would request higher current than the converter's nominal capacity.

D. Standard Linear Programming Model

Finally, the linear programming problem in its standard model as in (7) can be described by the following matrices:

$$\underline{c}_1 = [\mathbf{I}_{Lr}^{b^2} \quad \mathbf{I}_{Lv}^2 \quad \mathbf{I}_L^u{}^2], \quad \underline{A} = \begin{bmatrix} 1 & 1 & 1 \\ 1 & 0 & 0 \\ -1 & \frac{(1 - \lambda_D^{*2})}{\lambda_D^{*2}} & -1 \\ -1 & 0 & \frac{(1 - \lambda_N^{*2})}{\lambda_N^{*2}} \\ -1 & -1 & -1 \end{bmatrix}$$

$$\underline{x} = \begin{bmatrix} X_Q \\ X_D \\ X_N \end{bmatrix}, \quad \underline{b} = X_P \cdot \begin{bmatrix} \frac{(1 - \lambda^{*2})}{\lambda^{*2}} \\ \frac{\lambda_Q^{*2}}{1 - \lambda_Q^{*2}} \\ 1 \\ 1 \\ \frac{-(\mathbf{I}_{Lna} - \Delta \mathbf{I}_f)^2}{X_P} \end{bmatrix} \quad (16)$$

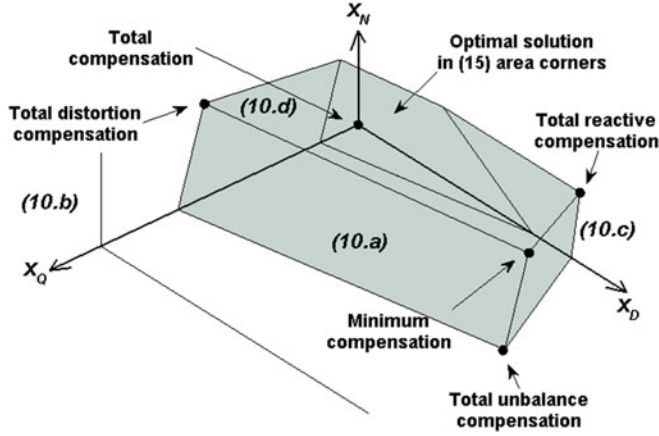


Fig. 2. Geometrical interpretation of optimized compensation linear problem.

where all the variables are nonnegative. Note that (15) has been multiplied by “-1” to fit in the standard SM. For single-phase applications, the quantities related to the unbalance term must be disregarded.

The solution found through SM represents the expected optimal source current terms (I_{sv}^*), mentioned in Section II-D. Therefore, the scaling coefficients are calculated by (5) and applied to (6) to generate the compensation current references for each phase m (i_{cm}^*).

E. Geometrical Interpretation

SM allows a geometrical interpretation of the linear problem, where the axes of Fig. 2 are the linear variables defined in Section III-A, and the constraint inequalities (10) and (15) shape the planes. The feasible solutions made up the bound volume (darker areas) and the optimal feasible solution is always located in one of its corners, driven by the objective function. In summary, SM moves along the boundary of the feasible area until reaching the optimal point. Note that the plane formed by (15) is variable with respect to the PES actual power and dependable on the available capability of EPP (ΔI_f).

By construction, the full compensation is located at the origin (X_Q, X_D, X_N) = (0, 0, 0), meaning that the unwanted currents have vanished at the source side. One can see that every corner in Fig. 2 has a particular meaning that represents different compensation strategies (e.g., total unbalance compensation). The optimal solution is always located in one of the area corners formed by (15) because it has the minimum value of the objective function and it complies with all the constraints.

It is also possible to define the minimum compensation point, which corresponds to the minimum collective rms current (I_c^{\min}) required to comply with the preset constraints. The minimum compensation point can be found by changing the weighting coefficients of matrix c to (17), which represents the minimum value of this objective function

$$c_2 = \begin{bmatrix} -1 & -1 & -1 \\ I_{Lr}^{b^2} & I_{Lv}^2 & I_L^u^2 \end{bmatrix}. \quad (17)$$

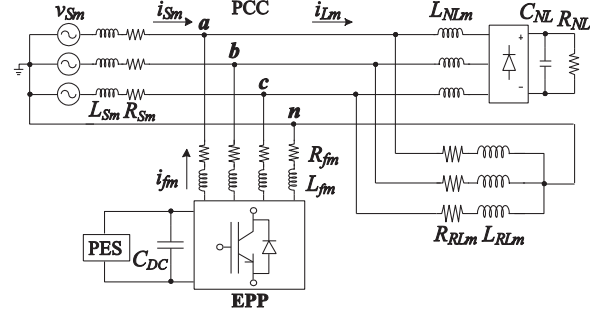


Fig. 3. Nonlinear and unbalance three-phase four-wire circuit.

TABLE I
PARAMETERS OF SOURCE VOLTAGE, LOAD, AND EPP

Load parameters	
$R_{RLa} = 4.4 \Omega$; $L_{RLa} = 15 \text{ mH}$; $R_{RLb} = 4.1 \Omega$; $L_{RLb} = 18 \text{ mH}$; $R_{RLc} = 3.7 \Omega$; $L_{RLc} = 30 \text{ mH}$.	
$L_{NLa} = 1 \text{ mH}$; $L_{NLb} = 1 \text{ mH}$; $L_{NLc} = 1 \text{ mH}$; $R_{NL} = 42 \Omega$; $C_{NL} = 2.35 \text{ mF}$.	
EPP parameters	
$L_{fm} = 1.5 \text{ mH}$; $R_{fm} = 0.1 \Omega$; $C_{DC} = 4.7 \text{ mF}$;	
Sinusoidal three-phase source (60 Hz)	
$V_{sa} = 127 \angle 0^\circ \text{ V}$; $V_{sb} = 127 \angle -120^\circ \text{ V}$; $V_{sc} = 127 \angle 120^\circ \text{ V}$; $L_{sm} = 0.5 \text{ mH}$; $R_{sm} = 0.05 \Omega$.	

TABLE II
LOAD CURRENT TERMS AND LOAD CONFORMITY FACTORS

Collective RMS load current values [A]				
$I_L = 32.6$	$I_{La}^b = 21.7$	$I_{Lr}^b = 23.3$	$I_{Lv} = 4.3$	$I_L^u = 5.8$
Load conformity factors				
$\lambda_L = 0.666$	$\lambda_{LQ} = 0.732$	$\lambda_{LD} = 0.132$	$\lambda_{LN} = 0.179$	

If ΔI_f becomes too limited up to not allowing the current compensation to comply with the requirements, which means $\Delta I_f < I_c^{\min}$, then the Simplex algorithm must be, temporally, disregarded and the EPP should compensate as much as possible, calculating the nonactive scaling coefficient as

$$k_{na} = \frac{\Delta I_f}{I_{Lna}} \quad (18)$$

and applying it to (6). This strategy corresponds to setting all the weighting coefficients equally, not assigning priority to compensation.

IV. THEORETICAL ANALYSIS AND SIMULATION RESULTS

To analyze the proposed strategy, the three-phase four-wire circuit of Fig. 3 has been considered. A three-phase four-leg inverter with wide bandwidth current control loop was connected to the system representing a three-phase distributed EPP. The source voltages (v_{sm}) and the three-phase load are shown in Table I. The respective CPT's load current terms and conformity factors are shown in Table II.

TABLE III
THEORETICAL RESULTS FOR MINIMUM COMPENSATION (MATLAB)

$\lambda^* = 0.920$	$\lambda_Q^* = 0.400$	$\lambda_D^* = 0.080$	$\lambda_N^* = 0.070$
$I_{sa}^b = 21.70$ A	$I_{sr}^{b,*\min} = 8.90$ A	$I_{sv}^{*\min} = 1.89$ A	$I_s^{u,*\min} = 1.65$ A
—	$k_Q = 0.618$	$k_D = 0.561$	$k_N = 0.716$
—	$I_{fr}^b = 14.40$ A	$I_{iv} = 2.41$ A	$I_f^u = 4.15$ A
$I_{sa}^b = 21.70$ A	$I_{sr}^b = 8.90$ A	$I_{sv} = 1.89$ A	$I_s^u = 1.65$ A
$\lambda^{pcc} = 0.920$	$\lambda_Q^{pcc} = 0.379$	$\lambda_D^{pcc} = 0.080$	$\lambda_N^{pcc} = 0.070$

A. Theoretical Results

The system of Fig. 3 was implemented in MATLAB code in order to evaluate the impact of the optimization algorithm on the EPP operation and its cost effectiveness, which is meant to express the capability to process more amount of power/current in an efficient way, without modifying the nominal capacity and cost of an electronic power converter. Let us consider a stiff grid where the PCC voltages are constant along the compensation process. Then, as an example, let us assume possible references for the conformity factors, such those indicated in Table III. These values were empirically defined based on the author's experience.

The minimum compensation objective function (17) applied to the SM (7) returns the minimum source current terms (second row in Table III), which are used to calculate the scaling coefficients (5). Finally, through (6), the compensation current references (i_{fm}^*) are generated. For this theoretical study, $i_{fm}^* = i_{cm}^{*\min}$, since for APF $I_{PES}^* \approx 0$. The EPP's current controller tracks i_{fm}^* minimizing the unwanted current terms at the source side (fifth row in Table III). Finally, the PCC conformity factors are calculated, and they match the requested factors.

As shown in Fig. 2, the minimum compensation point $(X_Q, X_D, X_N) = (8.90^2, 1.89^2, 1.65^2)$ requires 15.18 A of EPP's collective current (fourth row in Table III). Considering any other point, for example, $(X_Q, X_D, X_N) = (9.24^2, 0.0^2, 0.0^2)$, on which the PCC factors are $(\lambda^{pcc} = 0.916, \lambda_Q^{pcc} = 0.4, \lambda_D^{pcc} = 0, \text{ and } \lambda_N^{pcc} = 0)$ and also complies with the initial requirements (but with worse power factor), it needs 15.82 A. The difference of 0.64 A is negligible considering linear scale ($\approx 4\%$). However, it is significant in orthogonal scale ($\sqrt{15.82^2 - 15.18^2} = 4.45$ A), which represents about 30%. Then, applying the optimization to EPPs allows them to save extra 30% amperes for any additional task, such as compensation of unwanted currents.

1) Discussion of Prioritized Selective Compensation: Prioritization strategies for selective compensation schemes have been discussed in some papers such as [27]. Alfonso-Gil *et al.* [11] recommended equally ranking the disturbing current references. On the other hand, Singh and Verma [12] proposed to give preference first to harmonics, then to unbalance, and finally to reactive compensation. Herein, we have proposed to prioritize the highest collective rms value of load current, as in (9). Thus, assuming the feasible area of Table III (top row), the three proposals have been theoretically analyzed through the circuit of Fig. 3. In order to get similar behavior in terms of the relative importance given to the unwanted current terms, the proposal from [11] was performed by (18), which

TABLE IV
COMPARISON OF PRIORITIZATION OF SELECTIVE COMPENSATION

Proposal	Scaling coeffs.	Conf. factors	Currents [A]
[11]	$k_Q = k_D = k_N = 0.728$.	$\lambda = 0.956; \lambda_Q = 0.280; \lambda_D = 0.051; \lambda_N = 0.070$.	$I_s = 22.55; I_f = 17.68$.
[11]	$k_Q = k_D = k_N = 0.622$.	$\lambda = 0.920; \lambda_Q = 0.376; \lambda_D = 0.069; \lambda_N = 0.093$.	$I_s = 23.22; I_f = 15.10$.
[12]	$k_Q = 0.603; k_D = 1; k_N = 1$.	$\lambda = 0.920; \lambda_Q = 0.393; \lambda_D = 0.000; \lambda_N = 0.000$.	$I_s = 23.51; I_f = 15.67$.
Here	$k_Q = 0.618; k_D = 0.561; k_N = 0.716$.	$\lambda = 0.920; \lambda_Q = 0.379; \lambda_D = 0.080; \lambda_N = 0.070$.	$I_s = 23.31; I_f = 15.08$.

corresponds to setting all the weighting coefficients equally; while the proposal from [12] was performed by (9), replacing the weighting coefficients ($I_{Lr}^{b2}, I_{Lv}^2, I_L^{u2}$) to (10, 500, 100), as in [11]. The comparison results are depicted in Table IV.

From Table IV, one can notice that the solution proposed in [11] needs 17.68 A to attain the requested conformity factors, or 15.10 A to comply with the power factor. However, $\lambda_N = 0.093$ remains out of the feasible solution. The proposal in [12] needs 15.67 A to achieve the feasible solution. Conversely, the strategy proposed here requests 15.08 A to match all the preset conformity factors.

B. Simulation Results

To analyze the proposed methodology and the system dynamic behavior under distorted and asymmetrical voltages, the circuit shown in Fig. 3 was simulated using PSIM software and the linear programming using MATLAB (see appendix). The PCC voltages have been set to have 3% of third, fifth, and seventh harmonics, totalizing THD of 5.2%, and -3% (phase *a*) and 3% (phase *c*) of asymmetry. The same conformity factor references from Table III were considered.

1) PES Power Variation Under Nonideal Voltages Operation: Considering the EPP nominal collective current equal to 40 A, the PES power generation has been varied for three different cases. #1) $\Delta I_f \geq I_{Lna}$; #2) $I_{Lna} > \Delta I_f \geq I_c^{\min}$; #3) $\Delta I_f < I_c^{\min}$. The results are shown in Figs. 4 and 5.

At the beginning of the simulation (interval #1), EPP has enough available power capacity to compensate all the unwanted load currents. Thus, all the scaling coefficients are unitary, as well as λ . The performed compensation strategy is *resistive load synthesis* and not *sinusoidal current synthesis*, as discussed in Section I. Then, the source current waveforms are proportional to the PCC voltages and have about 5.2% of THD_{is} and low neutral current (see bottom of Fig. 5). The residual neutral current is related to the PCC voltage nonidealities (unbalances and third-order harmonics). As discussed in Section II-C, the CPT's factors are concentrated on the *load behavior* and not just on the current waveforms.

After 0.15 s (interval #2), PES increases its power generation forcing EPP to reduce its compensation rate (see ΔI_f on top of Fig. 4) because the active power injection is considered as the top priority in the optimization algorithm by means of (13). However, the optimized compensation operates at the optimal

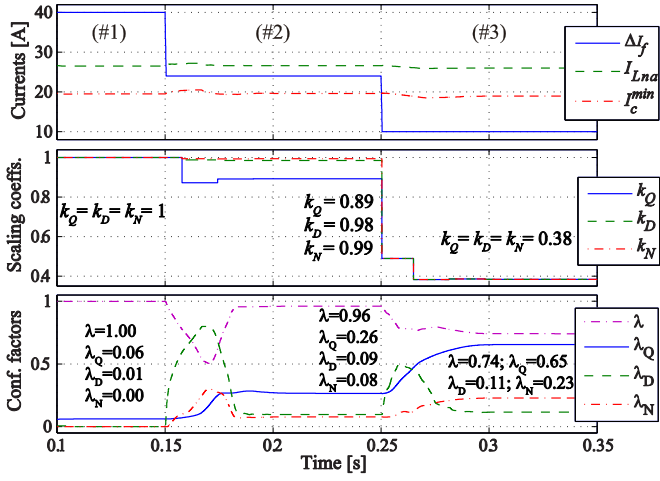


Fig. 4. Optimized compensation under PES power variation and non-ideal voltages operation. From top to bottom: currents, scaling coefficients, and PCC conformity factors.

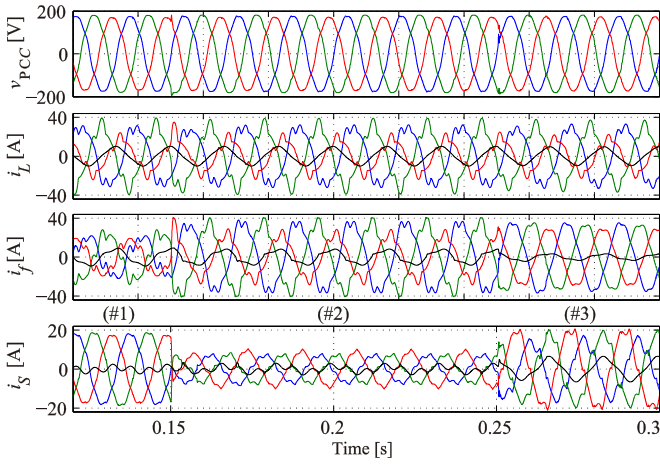


Fig. 5. Optimized compensation under PES power variation and non-ideal voltages operation. From top to bottom: PCC voltages, load, filter, and source current waveforms.

TABLE V

LOAD AND SOURCE CURRENT HARMONICS DURING PES POWER VARIATION UNDER NONIDEAL VOLTAGES OPERATION

Current	THD _{iL}	THD _{is} #1	THD _{is} #2	THD _{is} #3
phase a	17.40%	5.67%	13.15%	16.39%
phase b	17.02%	4.75%	16.77%	15.61%
phase c	22.92%	4.90%	10.35%	11.81%

point in steady state. It maximizes the compensation of the unwanted current terms, and practically complies with the required conformity factors.

After 0.25 s (interval #3), the power generation increases close to the PES nominal value. Then, EPP cannot comply anymore with the preset constraints and it must run using (18) to compensate as much unwanted currents as possible. See Table V to compare the THD_{is} of current source between the three simulated intervals.

With respect to the dynamic response aspect, the main objective of an OBA applied to compensation purpose is the operation

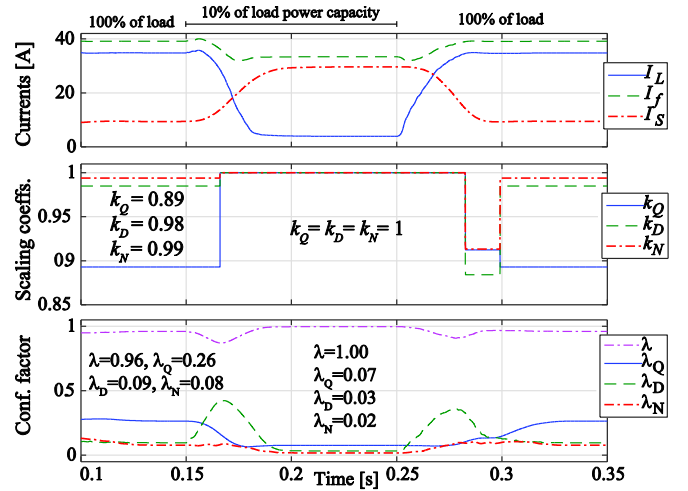


Fig. 6. Optimized compensation under heavy load variation. From top to bottom: currents, scaling coefficients, and PCC conformity factors.

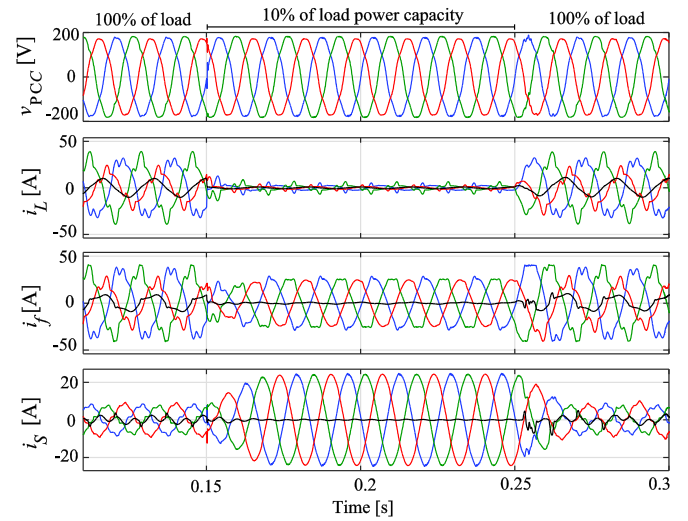


Fig. 7. Optimized compensation under heavy load variation. From top to bottom: PCC voltages, load, filter, and source current waveforms.

in steady state. That being said, the dynamic response of the scaling coefficients is mostly dominated by the rms algorithm used in (5) and (18), which was implemented using moving average filters (MAFs) with one fundamental cycle of time response. The optimization algorithm is updated once per fundamental cycle and the scaling coefficients steady state is achieved in two to three cycles (≈ 50 ms). The conformity factors have slower dynamic response due to the more complex calculation. However, these factors are used only in PCC analysis and not used into the optimized compensation strategy, which is an *online open-loop strategy*. That can be verified in Fig. 5, where the filter and source current waveforms show fast response.

2) Heavy Load Variation: In order to evaluate the effectiveness of the proposed compensation strategy under heavy load variation, a 90% load reduction and increase were set, respectively, at 0.15 and 0.25 s. The results are shown in Figs. 6 and 7, considering the previous distorted voltages.

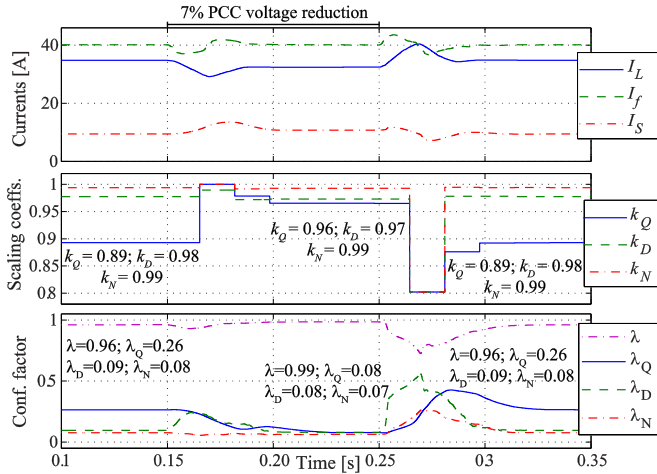


Fig. 8. Optimized compensation under nonideal voltage variation. From top to bottom: currents, scaling coefficients, and PCC conformity factors.

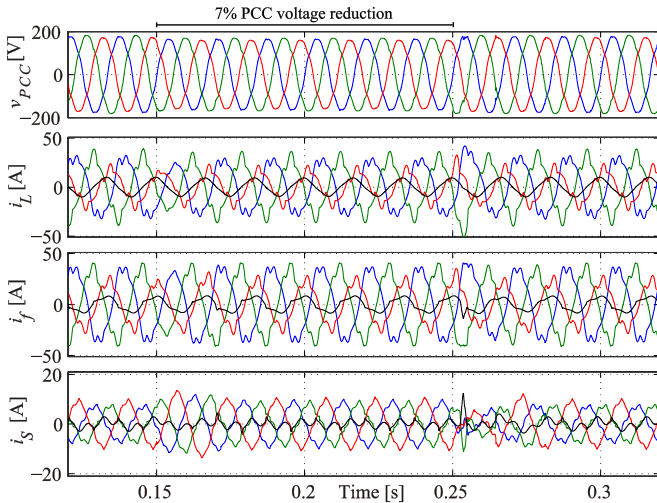


Fig. 9. Optimized compensation under nonideal voltage variation. From top to bottom: PCC voltages, load, filter, and source currents.

The beginning of this simulation matches with the interval #2 of the previous case study. At 0.15 s, the load is abruptly reduced to 10% of its nominal power, which increases the available capability (ΔI_f) for auxiliary services, allowing the EPP to perform full compensation of unwanted current terms ($k_Q = k_D = k_N = 1$ in Fig. 6). Observe in Fig. 7 that the generated active power is initially drained by the (local) full load, while during the load reduction it is injected to the grid, causing the magnitude of the source currents to increase. Finally, at 0.25 s, the load power returns to its initial value, and the algorithm restores to the same operating point as before the load reduction.

3) Voltage Source Variation: Again, using the previous distorted voltages, source voltages were set to have 7% voltage reduction at 0.15 s and restoration to their nominal value at 0.25 s. Figs. 8 and 9 show the results of the system dynamic response during a voltage variation.

At the beginning, EPP operates with partial compensation due to its capability. During the voltage reduction, the load demand

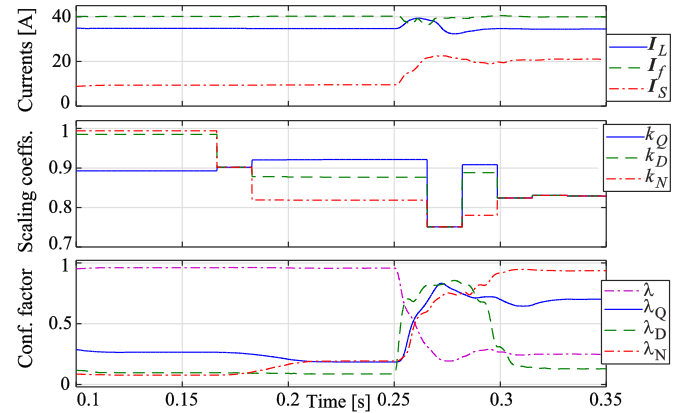


Fig. 10. Optimized compensation under source voltage fault. From top to bottom: currents, scaling coefficients, and PCC conformity factors.

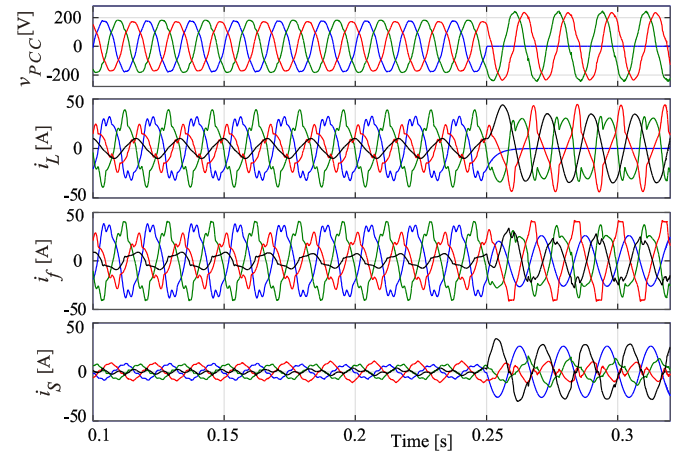


Fig. 11. Optimized compensation under source voltage fault. From top to bottom: PCC voltages, load, filter, and source current waveforms.

decreases and the ac power converter moves to an enhanced compensation point where the optimal solution is $\lambda = 0.99$, $\lambda_Q = 0.08$, $\lambda_D = 0.08$, and $\lambda_N = 0.07$. When the source supply is restored, the scaling coefficients return to their initial optimal point. Observe that EPP capability is fully exploited.

4) Conformity Factor Reference Change and Source Voltage Fault: In order to evaluate the proposed method following commands from distribution provider and during a practical fault occurrence, Figs. 10 and 11 show an online variation of conformity factor reference values and a short-circuit between phase and neutral.

The interval from 0.1 to 0.15 s corresponds to the interval #2 of Figs. 4 and 5. At 0.15 s, the conformity factor references were changed to $(\lambda^*, \lambda_Q^*, \lambda_D^*, \lambda_N^*) = (0.92, 0.20, 0.08, 0.15)$, and a fast transient response is observed. After 0.25 s, a short-circuit occurs between the phase *a* and neutral before PCC, and it is possible to see the current limiter, into the current control loop, restricts the EPP currents during the source voltage fault, while the proposed algorithm keeps running. In steady state during the short-circuit, the EPP performs the nonactive current compensation due to its limited capability. Of course, other



Fig. 12. Experimental laboratory-scale prototype.

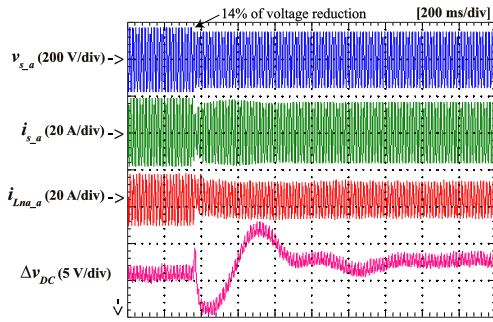


Fig. 13. Voltage variation. From top to bottom: phase *a*—PCC voltage, source and decomposed nonactive current, and dc-link voltage.

strategy during source fault detection could be applied, such as disconnection of DER or suspension of active power generation.

Other simulation analyses during sinusoidal operation and load dynamics can be found in [24].

V. EXPERIMENTAL RESULTS

To experimentally evaluate the proposed compensation scheme, the circuit of Fig. 3 and Table I were developed in laboratory. A general purpose and modular power conditioner prototype was used to configure the EPP, consisting of a three-phase four-leg voltage source inverter with IGBTs (SKM 100GB128D, driven by an SKPC 22/2). The digital control scheme was devised in a fixed-point digital signal processor (TMS320F2812). The three-phase converter is controlled as a shunt APF, driven by the PWM technique with sampling and switching frequency of 12 kHz. The duty cycle of the fourth leg is equal to 50%. Details of its parameters and current controller can be found in [28]. A picture of the laboratory-scale prototype is shown in Fig. 12.

At this point, it should be clear that handling the scaling coefficients is equivalent to vary the filter current references, and it has been extensively investigated for both APF and DER [6], [7], [29].

Fig. 13 shows the dc-link voltage response for an APF during a voltage reduction of 14%. In such case, as it is necessary to wait the variation of the dc voltage to start the compensation action,

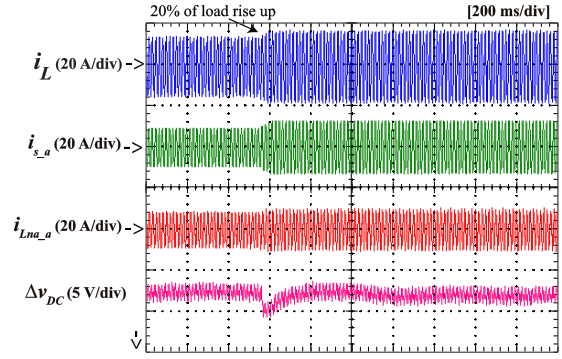


Fig. 14. Load change. From top to bottom: phase *a*—load current, decomposed nonactive current, source current, and dc-link voltage.

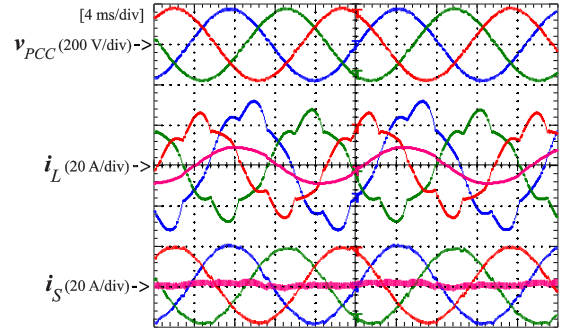


Fig. 15. Experimental results under sinusoidal and symmetrical voltages ($k_Q = k_D = k_N = 1$). From top to bottom: PCC voltages, load, and source currents.

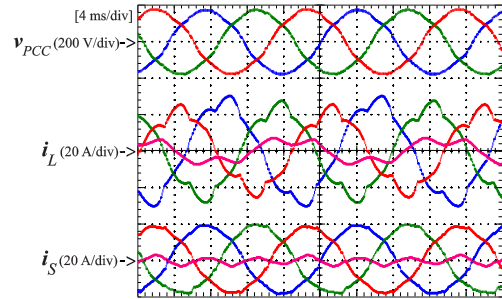


Fig. 16. Experimental results under distorted and symmetrical voltages ($k_Q = k_D = k_N = 1$). From top to bottom: PCC voltages, load, and source currents.

the dynamic response is typically tens of cycles. Similar behavior can be observed in Fig. 14 during a load power increasing of 20%. As discussed in [28], the dynamics is dominated by the current reference generator, and based on the literature review it is possible to conclude that the system reacts fast enough for steps of scaling coefficients (references) and the compensation can be attained in approximately 50 ms, as discussed in Section IV-B1 due to the updating frequency of the scaling coefficients (k_y).

To evaluate and compare the operation under sinusoidal and distorted voltage conditions, Fig. 15 shows the total nonactive compensation under ideal voltages ($\text{THD}_v = 0.9\%$), while Fig. 16 shows under distorted voltages (3% of third, fifth, and seventh harmonics, totalizing THD_v of 5.2%). Notice that the source currents track the voltage waveforms, characterizing a

TABLE VI
SOURCE CURRENT HARMONICS UNDER IDEAL AND DISTORTED
VOLTAGE CONDITIONS

Source current	THD _{is} (ideal voltages)	THD _{is} (distorted voltages)
i_{sa}	3.15%	5.86%
i_{sb}	2.20%	5.98%
i_{sc}	1.82%	5.97%

TABLE VII
PERFORMANCE OF THE POWER FACTOR REFERENCE TRACKING UNDER
DISTORTED AND SYMMETRICAL VOLTAGES

	λ^*	1.000	0.980	0.950	0.920	0.900
λ	0.997	0.982	0.957	0.928	0.912	
λ_Q	0.040	0.177	0.269	0.352	0.392	
λ_D	0.017	0.052	0.066	0.079	0.083	
λ_N	0.064	0.053	0.075	0.091	0.097	

resistive load synthesis, see Table VI. The remaining neutral source current is due to the deteriorated voltages. Besides, Table VII shows the system effectiveness to track the power factor reference under distorted and symmetrical voltages.

VI. CONCLUSION

This paper devised a linear problem to optimize the operation of EPPs, usually applied as APF or DERs with multifunctional capabilities. Using the proposed strategy, EPPs were able to minimize the reactive power, harmonic distortion, and load unbalances, optimizing the results in terms of the most important disturbing phenomena, while injecting available active power in case of DERs fed by intermittent power sources.

The proposed approach took advantage of the formulated linear problem, based on power quality requirements, to define a set of source performance constraints. It goes beyond other approaches that consider only current constraints. In addition, it enabled full exploitation of DERs capability.

The operation dynamics and steady-state behaviors had also been discussed. The former is dominated by the rms algorithm used in (5) and (18), and the latter depends on the implemented control strategy and current controller. However, it is faster than typically dc-link voltage regulators applied to APF or DER applications. The proposed solution had been evaluated through theoretical and experimental results, under ideal and nonideal voltage source operation, demonstrating its effectiveness in a scenario of practical interest. Finally, it is worth mentioning that the proposed methodology was applied here to a three-phase system, though it can be easily adapted to single-phase systems.

APPENDIX

% Optimized compensation

$$A = [1 \ 1 \ 1; 1 \ 0 \ 0; -1(1 - \lambda_D^*)/\lambda_D^* - 1; -1 \ 0(1 - \lambda_N^*)/\lambda_N^*; -1 \ -1 \ -1; -1 \ 0 \ 0; 0 \ -1 \ 0; 0 \ 0 \ -1]$$

$$b = [I_a^{b2} \cdot ((1 - \lambda^2)/\lambda^2); I_a^{b2} \cdot (\lambda_Q^*/(1 - \lambda_Q^*)); I_a^{b2}; I_a^{b2}; -(I_{na} - \Delta I_f)^2; 0; 0; 0]$$

$$x0 = [0; 0; 0]$$

$$x0 = \text{fmincon}(@\text{myfun_optimized_comp}, x0, A, b)$$

$$\text{function } f = \text{myfun_optimized_comp}(x)$$

$$f = x(1) \cdot (I_{Lr}^{b2}) + x(2) \cdot (I_{Lv}^{b2}) + x(3) \cdot (I_L^{u2})$$

end

% Minimum compensation

$$x = \text{fmincon}(@\text{myfun_minimum_comp}, x0, A, b)$$

$$\text{function } f = \text{myfun_minimum_comp}(x)$$

$$f = x(1) \cdot (-1/I_{Lr}^{b2}) + x(2) \cdot (-1/I_{Lv}^{b2}) + x(3) \cdot (-1/I_L^{u2})$$

end

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